TYPE H5N- 2~8 Digits Din 72x72, 72x144mm Programmable Digital Timer

Features

* 4~8D CPU BASED TIMER PROGRAMMABLE.
* 9 PIN DIP SW TO CONFIGURE UP/DOWN AUTO/MAN RESET & 8 DIFFERENT TIMER RANGE 00000.001S~999999.9H
* 7 OPERATION MODE OF AUTO RESET 0.1~5S & MANUAL RESET.
* REPETITION=0.05S SETTING=0% TEMPERATURE=50PPM/oC
* SENSOR POWER: 12VDC 60mA
* OUTPUT: 1C OR 1Ax2. RATING: 3A250VAC 100K OPERATIONS
* INPUT: GATE, RESET IN
* EEPROM MEMORY (OR POWER OFF RESET)

AVAILABLE TYPES With mark of "*" is DOWN timer.

Classification			Digital Timer		
Operation system			Timer limit, Integrating operation		
Preset			Yes	Yes	Not
Backup memory			Not	Yes	Yes
Digital Display			Yes	Yes	Yes
Type Nos. of Digits & steps of preset S=Single Preset D=Double Preset	3D	S	H5N-3D	H5N-3DM	
		D	H5N-3D3D	H5N-3D3DM	
	4D	S	H5N-4D	H5N-4DM	H5N-4M
		D	H5N-4D3D*	H5N-4D3DM*	
	5D	S	H5N-5D	H7N-4DM	H5N-5M
		D	H5N-5D2D*	H5N-5D2DM*	
		D	H5N-5D5D-L	H5N-5D5DM-L	
	6D	S	H5N-6D	H5N-6DM	H5N-6M
		D	H5N-6D6D-L	H5N-6D6D M-L	
	7D	S	H5N-7D	H5N-7DM	H5N-7M
		D	H5N-7D7D-L	H5N-7D7DM-L	
	8D	S	H5N-8D-L	H5N-8DM-L	H5N-8M-L
		D	H5N-8D8D-L	H5N-8D8DM-L	

-3D: 9M59S, 9M59S, 9H59M, 9.99S, .999S, 99.9M, 99.9H, 999M.
-4D: 59M59S, 99M59S, 99H59M, 99.99S, 9.999S, 999.9M, 999.9H, 9999M.
-5D: 9H59M59S, 999M59S, 999H59M, 999.99S, 99.999S, 9999.9M, 9999.9H, 99999M.
-6D: 99H59M59S, 9999M59S, 9999H59M, 9999.99S, 999.999S, 99999.9M, 99999.9H, 999999M.
-8D: 9999H59M59S, 999999M59S, 999999H59M, 999999.99S, 99999.999S, 9999999.9M, 9999999.9H, 99999999M.

One timer with 8-range. All ranges are refer to 8D.

U: Count up, from 0 to set value [n].
D: Count down, from set value [n] to 0.

PROGRAMMING OF SPECIFICATIONS AND TIMER RANGE. DIP SW POSITION: ●= UP ↑ (0), ○= DOWN ↓ (1)

Specifications↓ DIP SW No.→		1	2 3	4 5 6	7 8 9
Timing direction	UP	○			
	DOWN	●			
Operation modes	N-Latch			○ ○ ○	
	F-Overflow			● ○ ○	
	C-Recycle			○ ● ○	
	R-Recycle			● ● ○	
	K-Overflow			○ ○ ●	
	P-Recycle			● ○ ●	
	Q-Recycle			○ ● ●	
	H-High limit			● ● ●	
Timer range	9999H59M59S				○ ○ ●
	999999M59S				○ ● ○
	999999H59M				○ ● ●
	999999.99S				○ ○ ○
	99999.999S				● ○ ○
	9999999.9M				● ● ○
	9999999.9H				● ○ ●
	99999999M				● ● ●

Step 1: Removal of switch cover
Remove the SW cover in the Manner shown in the photograph.

With a screw driver raise and remove the switch cover at lower portion of the front.

Step 2: Selection of specifications
By changing the respective pin's position of the DIP SW inside the cover.
Various functional specification can be selected as shown in left table.

Step 3: Indication of specification items.
Select the appropriate rating label

Note:
1. Please manual reset after changing the pin's position, otherwise will work on previous functions.
2. DIP SW 2, 3 not in use, do not care it's position

SPECIFICATIONS

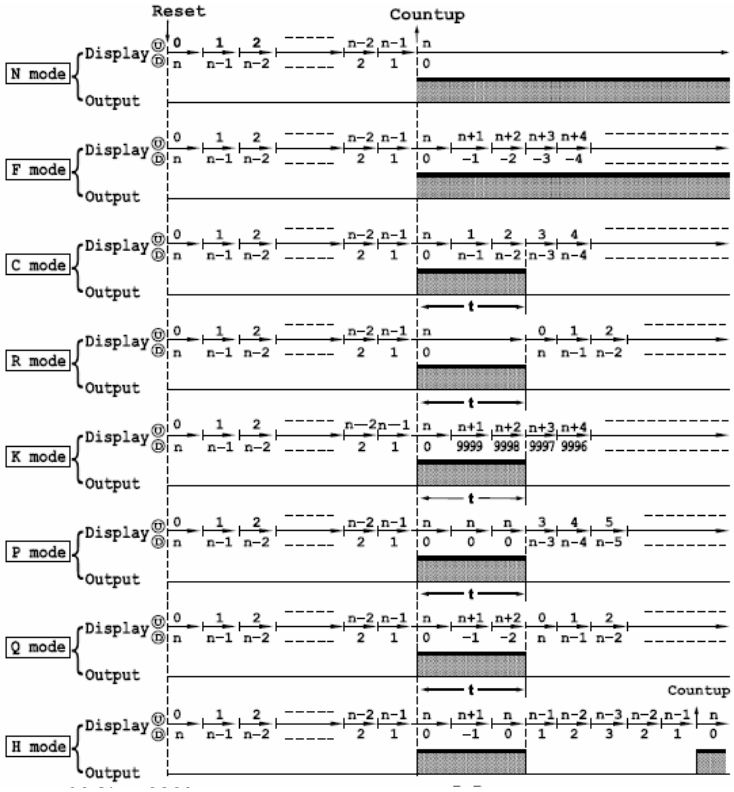
1. Rate voltage: Nominal voltage 110/220VAC 50/60Hz (P1,P2=110V, P1,P2=220V)
2. Operating voltage: 85~115% of rated voltage Power consumption: 3~5VA
3. Accuracy:+-50PPM/oC+-0.01S
4. Gate & Reset input: A.. Contact/SW input: Connect +12VDC by short or open to input
   B. Solid state voltage input: [H]=6~30VDC,[L]=0~2VDC, Impedance=4.7Kohm
5. Reset system: A. Power off reset minimum off 0.5S
   B. External & MAN. Reset time 0.5S
   C. AUTOmatic reset. Internal reset by each operation mode of C,R,K,P,Q&H
6. Control output: A. Single preset. Contact output 1SPDT & voltage output (Vout)
   B. Double preset (72x72). Contact output 2 SPST-NO (P4,P5=No.1 NO, P6P12=No.2 NO)
   C. Double preset (72x144). Contact output 2SPDT & voltage output (Vout)

   Output rating: Contact output=3A250VAC PF=1.0; Voltage output:=12VDC, impedance 4.7KΩ
7. Power source for external sensor: 12VDC+-10% 50mA

Note: Power off reset is not applicable to the types with suffix "M".

TIMING CHARTS (OPERATION MODES)

[n]: Set value, [t]: Output holding time (one shot time) 0.05S~5S adjustable.



*Time chart is for SINGLE preset O/P 1. O/P 2 slave to O/P 1.

N: Display & O/P latched while the Timer is up

F: Display overflow but O/P is latched while the timer is up

C: Display & timer reset to count, but O/P latched while timer is up. O/P reset after timing [t].

R: Display, timer & O/P is latched while timer is up. But all reset to count after timing [t].

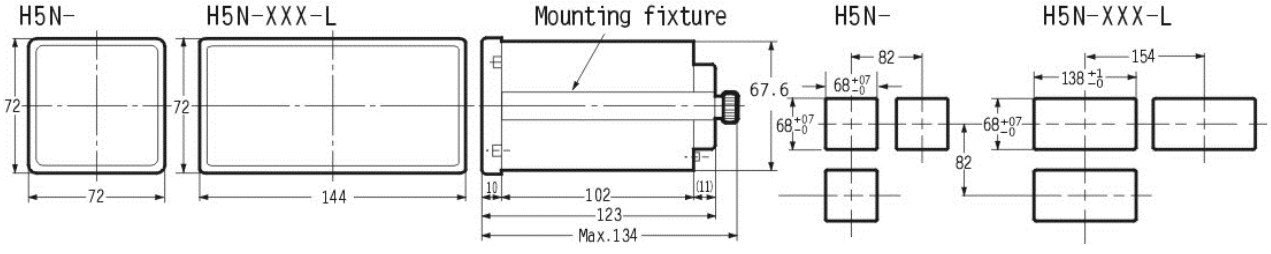
K: Display & timer overflow, O/P is latched while timer is up. O/P reset after timing [t].

P: Display & O/P latched, timer reset to count while is up. O/P reset, display update after [t].

Q: Display & timer overflow, O/P is latched while timer is up. But all reset after timing [t].

H: Display & timer overflow, O/P is latched while timer equal or greater than set [n]. when timer smaller than set [n]

DIMENSIONS(mm) & ONNECTIONS



Single preset **Fig.1**

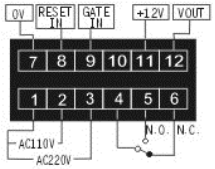
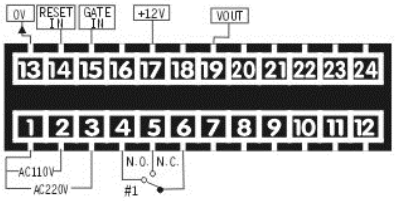


Fig.2



Double preset **Fig.3**

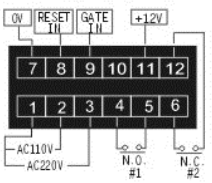


Fig.4